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L Number	Hits	Search Text	DB	Time stamp
1	7523	(714/25,30,43,44,724,728,733-735,738,739,742).ccls.	USPAT;	2004/07/22 16:41
			US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	
2	5	((714/25,30,43,44,724,728,733-735,738,739,742).ccls.) and (((((bidirectional\$2 bi-directional\$2 I/O) near4 (buffer device circuit)) or (interface near4 (circuit device))) same (self-test "self test" self\$1test)) and (compar\$3 near4 (prescribed expect\$2) near4 (signature data signal))) not (((((bidirectional\$2 bi-directional\$2) near4 (buffer device circuit)) or (interface near4 (circuit device))) same (self-test	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/07/22 16:48
_	370	"self test" self\$1test)) and (compar\$3 near4 (prescribed expect\$2) near4 (signature data signal))) interface and (Britt Harris Dooley).XA.	USPAT; US-PGPUB; EPO; JPO; DERWENT;	2004/07/20 15:18
•	44538	714/\$.ccls.	IBM_TDB USPAT; US-PGPUB; EPO; JPO; DERWENT;	2004/07/20 15:18
-	70	(interface and (Britt Harris Dooley).XA.) and 714/\$.ccls.	IBM_TDB USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2004/07/20 15:18
-	94	(interface near4 (circuit device)) and (Britt Harris Dooley).XA.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/07/20 15:19
-	32	((interface near4 (circuit device)) and (Britt Harris Dooley).XA.) and 714/\$.ccls.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/07/20 15:58
-	435	(((bidirectional\$2 bi-directional\$2) near4 (buffer device circuit)) or (interface near4 (circuit device))) same (self-test "self test" self\$1test)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/07/20 16:24
-	1	((((((bidirectional\$2 bi-directional\$2) near4 (buffer device circuit)) or (interface near4 (circuit device))) same (self-test "self test" self\$1test)) and (compar\$3 near4 (prescribed expect\$2) near4 (signature data signal))) and (\$6random near6 generat\$3)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/07/20 16:06
-	42	((((bidirectional\$2 bi-directional\$2) near4 (buffer device circuit)) or (interface near4 (circuit device))) same (self-test "self test" self\$1test)) and (compar\$3 near4 (prescribed expect\$2) near4 (signature data signal))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/07/20 16:25
-	626	(((bidirectional\$2 bi-directional\$2 I/O) near4 (buffer device circuit)) or (interface near4 (circuit device))) same (self-test "self test" self\$1test)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/07/20 16:24
-	51	((((bidirectional\$2 bi-directional\$2 I/O) near4 (buffer device circuit)) or (interface near4 (circuit device))) same (self-test "self test" self\$1test)) and (compar\$3 near4 (prescribed expect\$2) near4 (signature data signal))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/07/20 16:30

-	0	(((((bidirectional\$2 bi-directional\$2) near4 (buffer device circuit)) or (interface near4 (circuit device))) same (self-test "self test" self\$1test)) and (compar\$3 near4 (prescribed	USPAT; US-PGPUB; EPO; JPO;	2004/07/20 16:30
		expect\$2) near4 (signature data signal))) not  (((((bidirectional\$2 bi-directional\$2 l/O) near4 (buffer device	DERWENT; IBM_TDB	
		circuit)) or (interface near4 (circuit device))) same (self-test "self test" self\$1test)) and (compar\$3 near4 (prescribed		
-	0	expect\$2) near4 (signature data signal))) (((((bidirectional\$2 bi-directional\$2) near4 (buffer device circuit)) or (interface near4 (circuit device))) same (self-test	USPAT; US-PGPUB;	2004/07/20 16:30
		"self test" self\$1test)) and (compar\$3 near4 (prescribed expect\$2) near4 (signature data signal))) not	EPO; JPO; DERWENT;	
		(((((bidirectional\$2 bi-directional\$2 l/O) near4 (buffer device circuit)) or (interface near4 (circuit device))) same (self-test "self test" self\$1test)) and (compar\$3 near4 (prescribed	IBM_TDB	
-	9	expect\$2) near4 (signature data signal))) (((((bidirectional\$2 bi-directional\$2 l/O) near4 (buffer device	USPAT;	2004/07/22 16:47
		circuit)) or (interface near4 (circuit device))) same (self-test "self test" self\$1test)) and (compar\$3 near4 (prescribed expect\$2) near4 (signature data signal))) not	US-PGPUB; EPO; JPO; DERWENT:	
		(((((bidirectional\$2 bi-directional\$2) near4 (buffer device circuit)) or (interface near4 (circuit device))) same (self-test	IBM_TDB	
_	84	"self test" self\$1test)) and (compar\$3 near4 (prescribed expect\$2) near4 (signature data signal))) (((bidirectional\$2 bi-directional\$2 I/O) near4 (buffer device	USPAT;	2004/07/21 13:46
	:	circuit)) same (interface near4 (circuit device)) same (self-test "self test" self\$1test))	US-PGPUB; EPO; JPO;	200 1/07/27 10:10
_	1		DERWENT; IBM_TDB USPAT;	2004/07/21 10:08
-	1		US-PGPUB USPAT;	2004/07/21 10:08
-	1		US-PGPUB USPAT; US-PGPUB	2004/07/21 10:09
-	1		USPAT; US-PGPUB	2004/07/21 10:10
-	1		USPAT; US-PGPUB	2004/07/21 10:12
-	1		USPAT; US-PGPUB USPAT;	2004/07/21 10:12 2004/07/21 10:12
-	1		US-PGPUB USPAT;	2004/07/21 10:12
-	1		US-PGPUB USPAT; US-PGPUB	2004/07/21 10:13
-	1		USPAT; US-PGPUB	2004/07/21 10:13
-	1		USPAT; US-PGPUB USPAT;	2004/07/21 10:14
-	1		US-PGPUB USPAT;	2004/07/21 10:15
-	1		US-PGPUB USPAT; US-PGPUB	2004/07/21 10:15
-	1		USPAT; US-PGPUB	2004/07/21 10:15
-	1		USPAT; US-PGPUB	2004/07/21 10:15
-  -	1		USPAT; US-PGPUB USPAT;	2004/07/21 10:15 2004/07/21 10:25
_	1		US-PGPUB USPAT;	2004/07/21 10:26
			US-PGPUB	

	1			
-	1	·	USPAT; US-PGPUB	2004/07/21 10:26
-	1		USPAT; US-PGPUB	2004/07/21 10:27
-	. 1		USPAT; US-PGPUB	2004/07/21 10:27
-	1		USPAT;	2004/07/21 10:28
-	1		US-PGPUB USPAT;	2004/07/21 10:28
-	1		US-PGPUB USPAT;	2004/07/21 10:28
-	1		US-PGPUB USPAT;	2004/07/21 10:28
-	1		US-PGPUB USPAT;	2004/07/21 10:39
_	1		US-PGPUB USPAT:	2004/07/21 10:39
	1		US-PGPUB USPAT;	2004/07/21 10:39
	1		US-PGPUB	
		(10.0704007.0110.0507070.0110.050070.4.0	USPAT; US-PGPUB	2004/07/21 10:40
-	12	(US-6704897-\$ or US-6587976-\$ or US-6560734-\$ or US-6421801-\$ or US-5621739-\$ or US-5167020-\$ or US-5717701-\$ or US-6477674-\$).did. or (US-20020112206-\$ or US-20030080769-\$ or US-20030120989-\$ or US-20030101376-\$).did.	USPAT; US-PGPUB	2004/07/21 13:44
-	1	((US-6704897-\$ or US-6587976-\$ or US-6560734-\$ or US-6421801-\$ or US-5621739-\$ or US-5167020-\$ or US-5717701-\$ or US-6477674-\$).did. or (US-20020112206-\$ or US-20030080769-\$ or US-20030120989-\$ or US-20030101376-\$).did.) and MISR	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2004/07/21 13:45
-	1	((US-6704897-\$ or US-6587976-\$ or US-6560734-\$ or US-6421801-\$ or US-5621739-\$ or US-5167020-\$ or US-5717701-\$ or US-6477674-\$).did. or (US-20020112206-\$ or US-20030080769-\$ or US-20030120989-\$ or US-20030101376-\$).did.) and ("multiple input shift register" MISR)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/07/21 13:47
-	1	(((bidirectional\$2 bi-directional\$2 I/O) near4 (buffer device circuit)) same (interface near4 (circuit device)) same (self-test "self test" self\$1test)) and ("multiple input shift register" MISR)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/07/21 16:05
	3	((US-6704897-\$ or US-6587976-\$ or US-6560734-\$ or US-6421801-\$ or US-5621739-\$ or US-5167020-\$ or US-5717701-\$ or US-6477674-\$).did. or (US-20020112206-\$ or US-20030080769-\$ or US-20030120989-\$ or US-20030101376-\$).did.) and ("multiple input shift register" MISR signature)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/07/21 13:55
-	4	((US-6704897-\$ or US-6587976-\$ or US-6560734-\$ or US-6421801-\$ or US-5621739-\$ or US-5167020-\$ or US-5717701-\$ or US-6477674-\$).did. or (US-20020112206-\$ or US-20030080769-\$ or US-20030120989-\$ or US-20030101376-\$).did.) and ("multiple input shift register" MISR signature compact\$4)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/07/21 13:56
-	1	(bidirectional\$2 bi-directional\$2) same (interface near4 (circuit device)) same (self-test "self test" self\$1test) and (LFSR)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2004/07/21 14:09
-	13	(interface near4 (circuit device)) same (self-test "self test" self\$1test) and (LFSR)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/07/21 14:11

-	31	(interface near4 (circuit device)) same (self-test "self test" self\$1test) and ("linear feedback shift register" LFSR)	USPAT; US-PGPUB;	2004/07/21 14:11
		Solid Host, and ( initial recuback shift register Li ort)	EPO; JPO;	
			DERWENT;	
			IBM_TDB	
-	26	(interface near4 (circuit device)) same (self-test "self test"	USPAT;	2004/07/21 14:25
		self\$1test) and ("linear feedback shift register" LFSR) and	US-PGPUB;	
		("multiple input shift register" MISR signature)	EPO; JPO;	
			DERWENT; IBM_TDB	
_	596	("low voltage differential signals" LVDS "double data rate"	USPAT;	2004/07/22 13:48
		DDR) near4 interface	US-PGPUB;	
			EPO; JPO;	
			DERWENT;	
		//!law.yalkana differential aina alau LVDC !!day.lata data wata!!	IBM_TDB	0004/07/04 44 00
-	3	(("low voltage differential signals" LVDS "double data rate" DDR) near4 interface) and (self-test "self test" self\$1test) and	USPAT;	2004/07/21 14:28
		("linear feedback shift register" LFSR (pattern near2	US-PGPUB; EPO; JPO;	
	1	generat\$4)) and ("multiple input shift register" MISR signature)	DERWENT;	
		, , , , , , , , , , , , , , , , , , , ,	IBM TDB	
-	13	(("low voltage differential signals" LVDS "double data rate"	USPAT;	2004/07/21 14:28
		DDR) near4 interface) and (self-test "self test" self\$1test) and	US-PGPUB;	
		("linear feedback shift register" LFSR (pattern near2 generat\$4)"pseudo random generator")	EPO; JPO;	
		generativa) pseudo random generator )	DERWENT; IBM TDB	
_	3	(("low voltage differential signals" LVDS "double data rate"	USPAT;	2004/07/21 16:08
		DDR) near4 interface) and (self-test "self test" self\$1test) and	US-PGPUB;	200 0
1		("linear feedback shift register" LFSR (pattern near2	EPO; JPO;	
		generat\$4)"pseudo random generator") and ("multiple input	DERWENT;	
		shift register" MISR signature)	IBM_TDB	0004/07/04 44 50
<del>-</del>	2	(DE-19832307\$ DE-19901460\$).did.	USPAT; US-PGPUB;	2004/07/21 14:59
			EPO; JPO;	
			DERWENT;	
			IBM_TDB	
-	62	(I/O near4 interface) same self-test\$7	USPAT;	2004/07/21 16:06
			US-PGPUB;	
			EPO; JPO;   DERWENT;	
			IBM_TDB	
-	0	((I/O near4 interface) same self-test\$7) and ("linear feedback	USPAT;	2004/07/21 16:08
		shift register" LFSR (pattern near2 generat\$4)"pseudo random	US-PGPUB;	
		generator") and ("multiple input shift register" MISR signature)	EPO; JPO;	
			DERWENT;	
-	11	((I/O near4 interface) same self-test\$7) and ("linear feedback	IBM_TDB USPAT;	2004/07/21 16:11
	''	shift register" LFSR (pattern near2 generat\$4)"pseudo random	US-PGPUB;	2007/01/21 10:11
		generator")	EPO; JPO;	
			DERWENT;	
	F4	(///O moord interfere) come celf to the?\ == 1 (////O == == 1	IBM_TDB	0004/07/04 40 44
- ,	51	((I/O near4 interface) same self-test\$7) not (((I/O near4 interface) same self-test\$7) and ("linear feedback shift	USPAT; US-PGPUB;	2004/07/21 16:11
· .		register" LFSR (pattern near2 generat\$4)"pseudo random	EPO; JPO;	
		generator"))	DERWENT;	
			IBM_TDB	
-	5	("low voltage differential signals" LVDS "double data rate"	USPAT;	2004/07/22 10:47
		DDR) near4 self-test\$7 and (volt\$3 power)	US-PGPUB;	
			EPO; JPO; DERWENT;	
			IBM_TDB	
-	2221	(separate independent isolated) near4 ((supply near4 voltage)	USPAT;	2004/07/22 13:03
		or ((power voltage) near4 supply)) same (buffer I/O interface	US-PGPUB;	
		VDD\$4)	EPO; JPO;	
			DERWENT;	
	l		IBM_TDB	

-	1	((separate independent isolated) near4 ((supply near4 voltage) or ((power voltage) near4 supply)) same (buffer I/O interface VDD\$4)) and ("low voltage differential signals" LVDS "double data rate" DDR) same self-test\$7	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/07/22 10:48
-	50	((separate independent isolated) near4 ((supply near4 voltage) or ((power voltage) near4 supply)) same (buffer I/O interface VDD\$4)) and self-test\$7	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2004/07/22 10:49
-	1167	((separate independent isolated) near4 ((power voltage) near4 supply)) same (buffer I/O VDD\$4)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/07/22 14:07
_	3	(((separate independent isolated) near4 ((power voltage) near4 supply)) same (buffer I/O VDD\$4)) and DUT near4 board	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2004/07/22 13:16
-	1		USPAT; US-PGPUB	2004/07/22 13:14
-	1		USPAT; US-PGPUB	2004/07/22 13:14
-	1		USPAT; US-PGPUB	2004/07/22 13:14
*	5	(((separate independent isolated) near4 ((power voltage) near4 supply)) same (buffer I/O VDD\$4)) and ((DUT load) near4 board)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/07/22 13:23
-	90	(((separate independent isolated) near4 ((power voltage) near4 supply)) same (buffer I/O VDD\$4)) and test\$3 near4 (IC "integrated circuit" semiconductor)	USPAT; US-PGPUB; EPO; JPO; DERWENT;	2004/07/22 13:59
-	106	("low voltage differential signals" LVDS) same PLL	IBM_TDB USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/07/22 13:49
-	2	(("low voltage differential signals" LVDS) same PLL) and LFSR	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/07/22 13:49
-	133	(((separate independent isolated) near4 ((power voltage) near4 supply)) same (buffer I/O VDD\$4)) and interference	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/07/22 13:59
-	44	(((separate independent isolated) near4 ((power voltage) near4 supply)) same (buffer I/O VDD\$4)) and interference same supply	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/07/22 14:07
-	147	((power voltage) near4 supply) same ((buffer I/O VDD\$4) near4 ring)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2004/07/22 16:40
-	1		USPAT; US-PGPUB	2004/07/22 15:01
_	1		USPAT; US-PGPUB	2004/07/22 15:02
-	1		USPAT; US-PGPUB	2004/07/22 15:02

-	1	USPAT; 2004/07	7/22 15:03
		US-PGPUB US-PGPUB	
-	1	USPAT; 2004/07	7/22 15:03
		US-PGPUB US-PGPUB	
-	1	USPAT; 2004/07	7/22 15:03
		US-PGPUB	
-	1	USPAT; 2004/07	7/22 15:03
		US-PGPUB	